



DELHI TECHNOLOGICAL UNIVERSITY

Established by Govt. of Delhi vide Act 6 of 2009
(FORMERLY DELHI COLLEGE OF ENGINEERING)
BAWANA ROAD, DELHI-110042

Advt. No. D'IU-MeitY-MCEV/EED/02

5th August 2020

ADVERTISEMENT TO FILL UP PROJECT POSITIONS

A walk-in-interview for filling of project positions as per the details given below for the MeitY Sponsored R&D Project under Chief Investigator – Prof. Mini Sreejeth, Electrical Engineering Department (EED), Delhi Technological University (DTU) and Co-investigator – Prof. Indu S, Department of Electronics and Communication Engineering, DTU is scheduled to be held on **19th August 2020** in the Conference Hall of the EED.

- Title of the Project** : Development and deployment of motor controller for low to medium Power Electric Vehicles
- Sponsoring Agency** : Ministry of Electronics and Information Technology (MeitY)
- Details of Project Positions**

Sl. No	Désignation	Minimum Qualification	Experience	Nos.	Monthly Emoluments (₹)
a)	Engineer	a) B.E. / B. Tech. in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering OR M.E. / M. Tech. (Power Electronics / Electric Drives) b) 1 st Division in B.E. / M.E. level Desirable: NET/ GATE Qualified	At least 5 years industrial experience in design and development of Inverters as per details given in Job Description	1	80,000
b)	Technician	Diploma in Electrical Engineering/ Electrical and Electronics Engineering / Electronics and Communication Engineering	At least 2 years industrial experience as per details given in Job Description	1	17,924

- Age Limit** : 40 years for Engineer and 35 years for Technician. Age relaxation for candidates belonging to SC/ST/PH/OBC categories as per government norms.
- Duration** : Appointment to the above post shall initially be for a period of six months or for the duration of the project, whichever is earlier.

Mini Sreejeth
05/08/2020

(Prof. Mini Sreejeth)
Chief Investigator
MeitY Sponsored R&D Project
EED, DTU

Indu S
05/08/2020

(Prof. Indu S)
Co-Chief Investigator
MeitY Sponsored R&D Project
ECED, DTU

Uma Nangia
05/08/2020

Professor & Head
Electrical Engineering Department
Delhi Technological University
(Formerly Delhi College of Engineering)
Bawana Road, Delhi-110042

6. **Job description**

a)	Engineer	:	<ul style="list-style-type: none">• Embedded software development on 32 bits microcontroller platform for Inverter products• Software debugging and project documentation.• Excellent team work• Must have experience on RS232, RS485 and CAN bus communication
b)	Technician	:	<ul style="list-style-type: none">• Circuit assembling on general purpose PCB• Components soldering• Wiring
<p>• Note: The completion of the research project being time-bound, preference will be given to candidates who are willing to join the Project with immediate effect and willing to work beyond the call of normal duty.</p>			

7. Candidates before appearing for the Walk in Interview shall ensure that they are eligible for the position they intend to apply.
8. Candidates applying for the above post are requested to report on **19th August 2020 at 10:30 am** for registration, verification of documents, test / interview at the **Department of Electrical Engineering, DTU**. Candidates reporting **after 11:00 am will not be entertained**.
9. Candidates desirous to appear for the Walk in Interview should bring a set of self-attested copies of all certificates, testimonials, the latest CV and a passport sized colour photograph. The Application should include: -
 - Details of degree/certificates obtained starting from Class X (or equivalent) clearly indicating the aggregate percentage / CGPA
 - Experience in research, industrial field, academics and others.
10. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of walk in interview for verification.
11. Please note that no TA/DA is admissible for attending the walk-in interview.
12. The interview will be held in **Conference Hall of Department of Electrical Engineering, DTU** on **19th August 2020 at 11:00 am**.
13. The competent authority reserves the right to reject the candidature of any or all of the candidates without assigning any reason thereof.

Mini Sreejeth
05/08/2020

(Prof. Mini Sreejeth)
Chief Investigator
MeitY Sponsored R&D Project
EED, DTU

Indu S
05/08/2020

(Prof. Indu S)
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