

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली  
 हाऊ खास, नई दिल्ली -110016  
 (औद्योगिक अनुसंधान एवं विकास इकाई)  
 INDIAN INSTITUTE OF TECHNOLOGY DELHI  
 Hauz Khas, New Delhi-110016  
 (Industrial Research & Development Unit)

No. IITD/IRD/RP03754G/247742

Dated: 04/08/2020

**Advertisement No.: IITD/IRD/122/2020**

Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा।

Title of the Project	Flexible Inorganic Devices for Backplane and Sensor Node Electronics (RP03754G)	
Funding Agency	Science and Engineering Research Board (SERB), DST	
Name of the Project Investigator	Prof. Madhusudan Singh	
Deptt./Centre	Department of Electrical Engineering	
Duration of the Project	Upto:05/06/2022	
Post (s)	Consolidated fellowship / Pay-slab	Qualifications
Project Scientist (1)	Rs.35,400-37,900-40,400-43,600-46,800-50,000/- p.m. plus HRA @ 24%	Applicant must: 1. have earned their Ph. D. degree from a top engineering research program in India or overseas (must have defended their thesis prior to the interview, and be available to join immediately) in the field of optoelectronic devices/detectors/backplane fabrication, or closely allied fields. 2. inclusive of Ph. D. degree, have at least 5 years of experience in an intensive devices program with extensive hands on device fabrication and equipment maintenance experience; 3. have the following required experimental skills: sputtering, thermal annealing, X-ray diffraction and measurement of detector or PV efficiencies; 4. have the following desirable experimental skills: FESEM and ellipsometry; 5. be conversant with design tools: Cadence/Mentor Graphics/Tanner with at least one of Verilog/ VHDL; 6. be comfortable with writing data analysis / reduction code: Python/ MatLab / Fortran 95/C++ etc; 7. show clear evidence of physical reasoning, experimental data analysis skills, and the ability to work in an interdisciplinary team; 8. have at least 10 journal articles in peer-reviewed and respectable journals in the field (published or accepted); 9. have international conference experience (in India or overseas); and 10. have mentorship experience with junior students.

The candidates who are interested to apply for the above post should download Form No. IRD/REC-4 from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with additional information regarding educational qualifications by e-mail to Prof. Madhusudan Singh at email id: [msingh@ee.iitd.ac.in](mailto:msingh@ee.iitd.ac.in):

- Subject: [Application for Project Scientist Position (Advertisement ID)] <Your Name>, <Name of Ph. D. granting University/Institute> (<Year of Ph. D. defense>)  
For example: [Application for Project Scientist Position (122-2020)] Sunil Kumar, IIT Madras (2020)
- Body of the email: If you wish to include a cover letter, you may write it in the body of your email as plain text.
- Attachment: the email must include a **single** combined PDF file as an attachment with **only** the following contents:
  - (mandatory) **academic CV** (max. 4 pages) clearly listing your academic degrees, name(s) of thesis advisors, teaching experience, experimental skills (with approximate length of hands-on experience on each tool), job/project experience, publications/conferences/patents, any enrichment courses, and any certifications/other relevant information,
  - (mandatory) brief **one-page technical write-up** explaining your present research (pure text, no figures),
  - (mandatory) **transcripts** for your bachelors and masters degree coursework,
  - (mandatory) **application form** (REC-4) (see above),
  - (optional, if applicable) scan of any certificate pertaining to your reservation category, and,
  - (mandatory, if you have defended your Ph. D. thesis but not yet received your degree) scan of a provisional certificate from your University / advisor (on official letterhead) attesting to the fact that you have successfully defended your doctoral thesis.

The attachment must not exceed 800 KB in size. Please do NOT include your thesis documents (for any degree), scans of degrees, recommendation letters, or high school certificates/transcripts, or any unnecessary graphics/scans in the attachment.

If you need to compress your PDF document to meet the above size constraint, you may use any online (example: <https://docupub.com/pdfcompress/>) service, or offline software. Applications that use any format (other than PDF) will not be entertained. In particular, do NOT send any Word files (.doc or .docx). Non-PDF files will not be opened.

Contd.....

The requirements for the position are listed clearly above. In the rare case of any needed clarification on eligibility regarding the above position, the candidate may contact Prof. Madhusudan Singh at email id: [msingh@ee.iitd.ac.in](mailto:msingh@ee.iitd.ac.in).

**IIT Delhi reserves the right to reject any incomplete applications that are not compliant with the above instructions, or to dismiss any hire if they are later found to have omitted or misrepresented a material fact on their application. IIT Delhi also reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) alongwith the online interview details. Only short-listed candidates will be called for online interview.**

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. The last date for submitting the completed applications by e-mail is 21/08/2020 by 5.00 p.m (IST).



सहायक कुलसचिव, आईआरडी

**वितरण**

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Madusudan Singh, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC
- Dr. Harshita Bhatnagar, RD Coordinator, (R&D) Wing