

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/RP03754G/247590

Dated: 01/08/2020

Advertisement No.: IITD/IRD/122/2020


Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों से आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर समेकित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा.

Title of the Project	Flexible Inorganic Devices for Backplane and Sensor Node Electronics (RP03754G)	
Funding Agency	Science and Engineering Research Board (SERB), DST	
Name of the Project Investigator	Prof. Madhusudan Singh	
Deptt./ Centre	Department of Electrical Engineering	
Duration of the Project	Upto:05/06/2022	
Post (s)	Consolidated fellowship / Pay-slab	Qualifications
Project Scientist (1)	Rs.35,400-37,900-40,400-43,600-46,800-50,000/- p.m. plus HRA @ 24%	Applicant must: 1. have earned their Ph. D. degree from a top engineering research program in India or overseas (must have defended their thesis prior to the interview, and be available to join immediately) in the field of optoelectronic devices/detectors/backplane fabrication, or closely allied fields. 2. inclusive of Ph. D. degree, have at least 5 years of experience in an intensive devices program with extensive hands on device fabrication and equipment maintenance experience; 3. have the following required experimental skills: sputtering, thermal annealing, X-ray diffraction and measurement of detector or PV efficiencies; 4. have the following desirable experimental skills: FESEM and ellipsometry; 5. be conversant with design tools: Cadence/Mentor Graphics/Tanner with at least one of Verilog/ VHDL; 6. be comfortable with writing data analysis / reduction code: Python/ MatLab / Fortran 95/C++ etc; 7. show clear evidence of physical reasoning, experimental data analysis skills, and the ability to work in an interdisciplinary team; 8. have at least 10 journal articles in peer-reviewed and respectable journals in the field (published or accepted); 9. have international conference experience (in India or overseas); and 10. have mentorship experience with junior students.

The candidates who are interested to apply for the above post should download **Form No. IRD/REC-4** from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. **by e-mail with advertisement No. on the subject line to Prof. Madhusudan Singh at email id: msingh@ee.iitd.ac.in**

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) **alongwith the online interview details. Only short-listed candidates will be informed for online interview.** In case any clarification is required on eligibility regarding the above post, the candidate may contact **Prof. Madhusudan Singh at email id: msingh@ee.iitd.ac.in**

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है. एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआरडी मानदंडों के अनुसार तय किया जाएगा। **The last date for submitting the completed applications by e-mail is 21/08/2020 by 5.00 p.m.** The shortlisted candidates who are called for interview should bring original certificates (both professional and academic) with a recent passport size photograph at the time of interview.


सहायक कुलसचिव, आईआरडी

वितरण

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt. be brought to the notice of the staff working in your Deptt./Centre/Unit To put advertisement at IITD website.
- Webmaster, IRD :
- Notice Boards
- Advertisement file
- Prof. Madhusudan Singh, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC
- Dr. Harshita Bhatnagar, RD Coordinator, (R&D) Wing